

Patent 6,979,607

10/721585

PATENT

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**IN UNITED STATES PATENT AND TRADEMARK OFFICE**

Patent No.: 6,979,607-02

Docket No: 1303.017US2

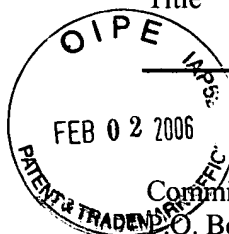
Issue Date: December 27, 2005

Patentee: Leonard Forbes et al.

Customer No.: 21186

Confirmation No.: 5778

Title: TECHNIQUE TO CONTROL TUNNELING CURRENTS IN DRAM CAPACITORS, CELLS, AND DEVICES



**REQUEST FOR CERTIFICATE OF CORRECTION**

**Certificate**

**FEB 06 2006**

**of Correction**

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It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. Two copies of the text of the Certificate in the suggested form are enclosed.

**Pursuant to 1.20(a), enclosed please find a check in the amount of \$100.00.**

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

The Examiner is authorized to charge any additional fees or credit overpayment to Deposit Account No.19-0743.

Respectfully Submitted

LEONARD FORBES ET AL.

02/03/2006 MWOLDGE1 00000074 6979607

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By their Representatives,

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
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Date: 1-30-06

By: [Signature]  
David C. Peterson  
Reg. No: 47,857  
DCP:rbs

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**FEB 07 2006**



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Leonard Forbes et al.

Examiner: Everhart, Caridad

Patent No.: 6,979,607 B2

Group Art Unit: 2891

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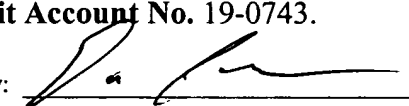
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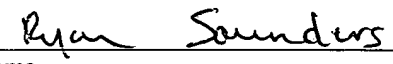
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SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer No: 21186

By:   
Name: David C. Peterson  
Reg. No. 47,857  
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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,979,607 *B2*

Page (1) of 1

DATED : December 27, 2005

INVENTOR(S) : Forbes et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On the face page, in field (56), under "Other Publications", in column 2, line 3, after "Digest" delete "." and insert - - , - - , therefor.

On page 2, in field (56), under "Other Publications", in column 1, line 8, delete "Tansistors" and insert - - Transistors - - , therefor.

On page 2, in field (56), under "Other Publications", in column 1, line 9, after "Electron" delete "." and insert - - , - - , therefor.

On page 2, in field (56), under "Other Publications", in column 1, line 9, after "Meeting" delete "." and insert - - , - - , therefor.

In column 1, line 11, after "following" insert - - co-pending, - -.

In column 1, line 16, delete "ULTRATHIN".

In column 5, line 6, delete "k." and insert - - K. - - , therefor.

In column 5, lines 27-28, delete "molydenum" and insert - - molybdenum - - , therefor.

In column 7, line 44, after "FIG. 6" delete "can similarly".

In column 7, line 44, delete "another" and insert - - one - - , therefor.

In column 7, line 45, delete "600, or" and insert - - including a - - , therefor.

In column 7, line 46, after "FIG. 6" insert - - , - - .

In column 7, lines 56-62, delete "In this alternative embodiment, the gate includes a metal gate selected from the group consisting of cobalt, nickel, ruthenium, rhodium, palladium, iridium, platinum and gold. Further, the metal gate can include a metallic nitride gate selected from the group consisting of titanium nitride, tantalum nitride, tungsten nitride, and molybdenum nitride." and insert - - A channel 607 is located between the first and the second source/drain regions, 608 and 610. A gate 609 opposes the channel 607. According to the teachings of the present invention, the gate 609 includes a gate 609 formed of a material having a large work function. A gate insulator 613 separates the gate from the channel. - - , therefor.

In column 8, line 32, delete "rubinium," and insert - - ruthenium, - - , therefor.

In column 10, line 43, delete "Method" and insert - - Methods - - , therefor.

MAILING ADDRESS OF SENDER:

PATENT NO. 6,979,607

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